

U.S. PAT. & T.M. OFF.
10/05/01
01/18/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	CAU	EXAMINER
10050171	01/18/2002	255 428	503	2811 2812	<i>Watanabe</i>

**APPLICANTS: Watanabe Kenichi;

Watanabe

**CONTINUING DATA VERIFIED:

None

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** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-271416 09/07/2001

NEW

PG-PUB ☐ DC NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

MEW

ATTORNEY DOCKET NO

020029

TITLE : Semiconductor device capable of suppressing current concentration in pad and its manufacture method

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

Assistant Examiner

ISSUE FEE

Amount Due

Date Paid

☒ TERMINAL

DISCLAIMER

CLAIMS ALLOWED

Total Claims

Print Claim for
O.G.

DRAWING

Sheets Drawn

Figs. Drawn

Print Fig.

Primary Examiner

PREPARED FOR ISSUE

Application Examiner

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